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Dudeck 3-6-11-34-7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Dudeck et al.
Case: 3-6-11-34-7
Serial No.: 10/764,000
Filing Date: January 23, 2004
Group: 2827
Examiner: Son Luu Mai

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *Gene Manning* Date: November 14, 2005

Title: Method and Apparatus for Reducing Leakage Current in a Read Only Memory Device Using Transistor Bias

TRANSMITTAL LETTER

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

1. Amendment and Response to Office Action;
2. Marked-Up Figures 4 and 5; and
3. Substitute Figures 4 and 5.

There is no additional claim fee due in conjunction with the Amendment. In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

Kevin M. Mason

Date: November 14, 2005

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Signature: *Son L. Mai* Date: November 14, 2005

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AMENDMENT AND RESPONSE TO OFFICE ACTION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the outstanding Office Action, dated Aug. 12, 2005, please amend the above-identified patent application as follows: